

EUV free-electron laser requirements and considerations for semiconductor manufacturing Erik R. Hosler¹, Obert R. Wood II¹ and William A. Barletta² ¹GLOBALFOUNDRIES

²Massachusetts Institute of Technology



EUV free-electron laser requirements and considerations for semiconductor manufacturing

Current EUV Lithography Status

2 Why do we need FELs?

1

3 Lithographer's Perspective on FELs

4 FEL Research Developments

5 GLOBALFOUNDRIES FEL Research



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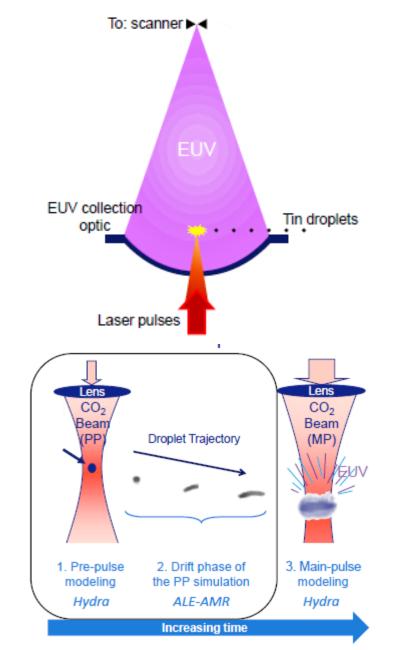
5 GLOBALFOUNDRIES FEL Research



EUV Source for HVM – State of the Art

- Laser-produced plasma (LPP) source
 - High-power CO₂ laser
 - Sn droplet source
 - Complex feedback and control systems
- ASML/Cymer
 - 125 W sources deployed to customers
 - ~200 W on test benches
- 'When', not 'if'
 - Insertion discussed @ 7nm 2018/2019
 - No longer 'an exercise'

Pirati, A., et al. SPIE Proc. EUVL, 2016. Purvis, M., et al. SPIE Proc. EUVL, 2016.

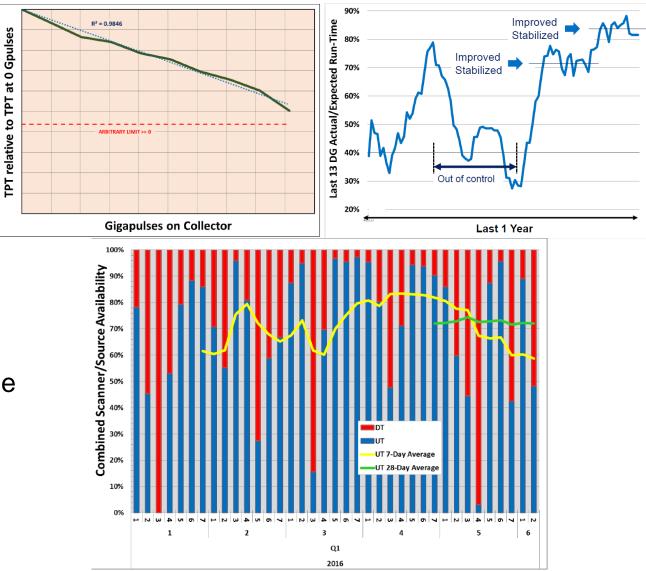


Laser-produced plasma source challenges in HVM

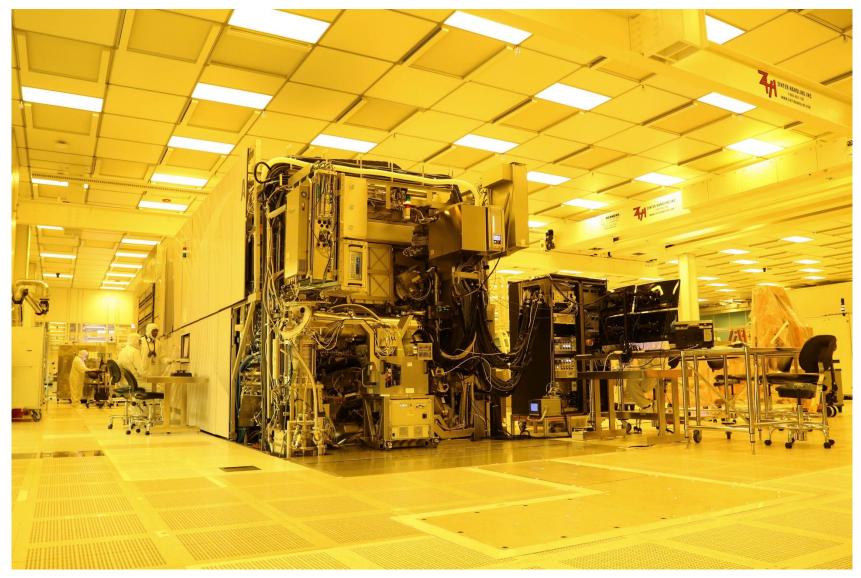
- Availability and consistent performance
 - Tin
 - Collector
 - Droplet Generator
 - Laser
 - Control Systems

Progress and Confidence

 "Introduction is gated by the number of technology development wafers that can be consistently processed..." -Intel



Albany EUV Scanner



Dan Corliss, IBM Research Alliance. 2016.

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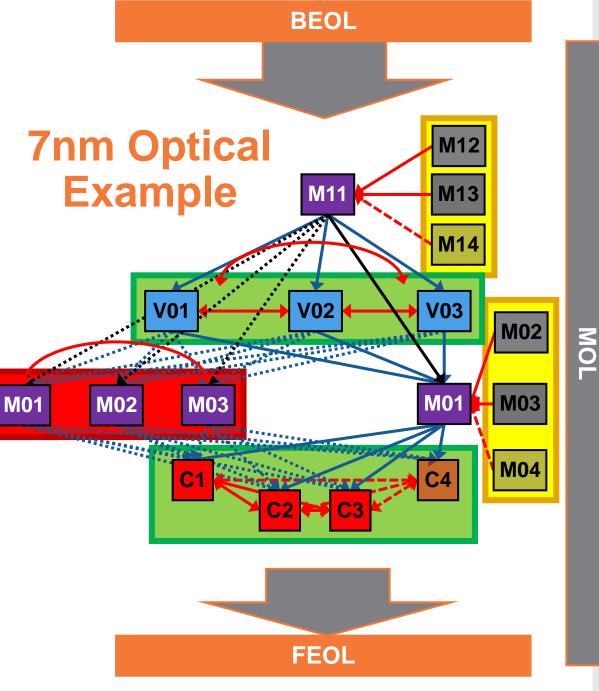
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Why EUV?

Cost Effectiveness

- EUV insertion...7nm? 5nm? Beyond?
 - LELE → LELELE → SADP/SAQP
- Productivity is key
- Need lithography performance at dose



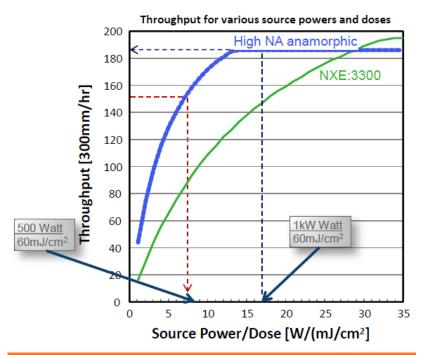
Extreme-Ultraviolet Lithography High-Volume Manufacturing

- EUV for **N3** and beyond: high-NA or multi-patterning?
- N3 lithography requirements[†]
 - 10+ EUV single exposure layers
 - Dose >50 mJ/cm²
 - $2k \rightarrow 5k+$ wafer starts per day

• N3 requires an EUV source power ~500 → 1000 W

- High availability/reliability and throughput
- Free-Electron Lasers (FELs)
 - Potential for deployment by 2024

[†] IMEC Technology Roadmap van Schoot., J., et al. EUVL 2015. Maastricht, Netherlands Hosler, E.R., et al. SPIE Proc. EUVL. 2015.



High-NA EUV Scanner Throughput

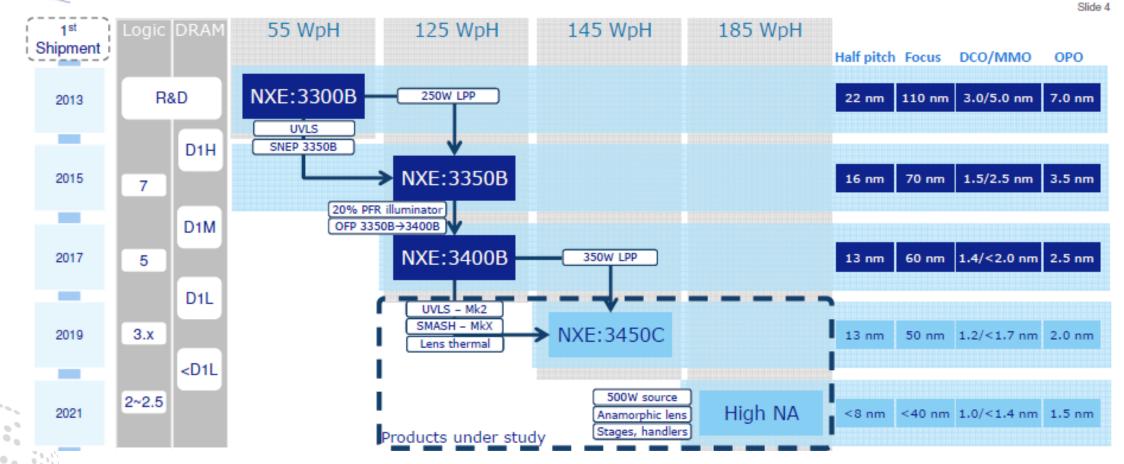


ASML EUV Roadmap

NXE extension roadmap to optimize capital efficiency

ASML

Public



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Lithographic Requirements

- An EUV FEL must power multiple scanners simultaneously
- FEL EUV source must operate with an availability of 100%
 - Redundancy of high-risk/low-cost machine components
 - Minimizing stress on long replacement time components
 - Two FELs must be run simultaneously!

Cost

- FEL EUV program must be substantially cheaper (depreciation + OpEx) and more powerful than an equivalent number of LPP sources to justify development risk
- How many EUVL sources are required for HVM?
 - 7 nm Logic roadmap from IMEC says 5-10 EUV layers
 - 3-4 L/S (>25 mJ/cm²), 2-6 contact (>35 mJ/cm²) = 10x 250 W LPP tools for ~50k wafers/month
- Other: FEL Specific
 - Beam Distribution
 - High power, splitting efficiency
 - Power management and Facility Size
 - · On mask, on mirrors, on wafer, into beam dump, and electrical power
 - Integrate with existing fab architecture
 - Coherence
 - Manipulate at scanner or within distribution system?
 - Harmonic mitigation scheme
 - FELs produce a few percent of the fundamental power in higher harmonics
 - Wavelength stability? Optical bandwidth? Power stability (Dose repro <0.2%)?

An EUV FEL for Lithography

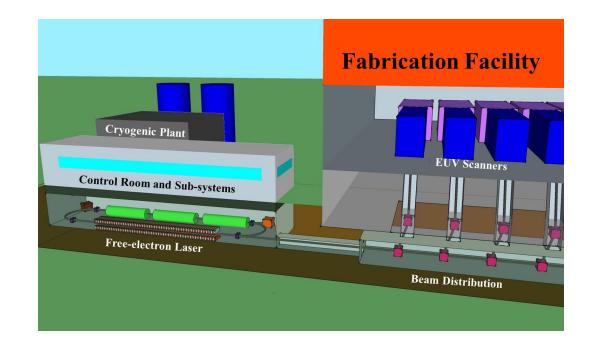
- Joint paradigm shift
 - FEL not for the single user...
 - Lithography tool does not have a single source...
- Single purpose facility
 - Support entire EUV lithography sector
 - No need for adjustable wavelengths
 - Need high-average power
 - Minimal coherence, maximum divergence

Pulse duration

- No strict requirement for lithography...besides avoiding ablation
- Necessarily short as a result of FEL efficiency
- Long pulse duration EUV FEL emission will increase optics lifetime → limit peak power
- Scaling to 6.x nm?
 - Necessary changes in exposure equipment, mask blanks and mirror may represent an economic roadblock
 - EUV self-aligned techniques or multi-pattering

Considerations for a free-electron laser-based extreme-ultraviolet lithography program

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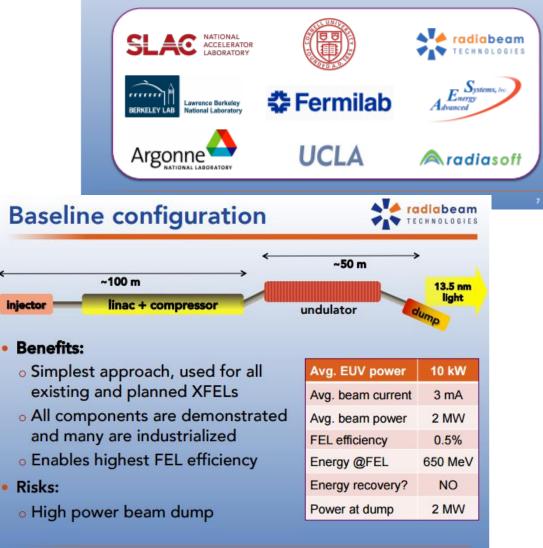
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Stanford-Berkeley-FermiLab-RadiaBeam

- Leverage US expertise:
 - X-ray FEL (LCLS)
 - Particle colliders (FERMI)
 - Energy Recovery Systems (Cornell)
 - High-brightness e⁻ gun (Berkeley, Cornell)
 - Industrialization and integration
- Minimize variables and risk for fast development
- · 'Off the shelf' components

US EUV FEL consortium

- Consortium of national labs, universities, and industrial suppliers was formed to explore industrial FEL opportunities
- · Represents large part of the FEL community in the US

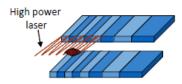


TESSA

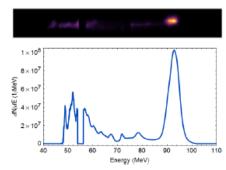
• How to drive higher efficiency?

Inverse FEL

- Broader view of undulator interaction: laser and e-beam, when at resonance inside the wiggler exchange energy)
- FEL is an amplifier and decelerator (laser absorbs energy from e-beam, albeit at a moderate rate < 1 MeV/m)
- IFEL is an accelerator: e-beam absorbs energy from the high power laser
- IFEL demonstrated energy exchange rate ~ 100 MeV/m, and design studies indicate possibility of 1 GeV/m
- Can we run IFEL in reverse?



In an IFEL the electron beam absorbs energy from a radiation field.

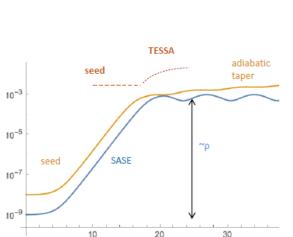


UCLA results from RUBICON experiments J. Duris et al, *Nature Comm.* 5, 4928, 2014





- E-beam rapid deceleration → laser amplification
- Requires seed pulse of high intensity (larger than P_{SAT})
- Tapering is optimized using proprietary GIT algorithm (Genesis Informed Tapering)



Amplified output pulse

Decelerated e-beam

Strongly tapered undulato

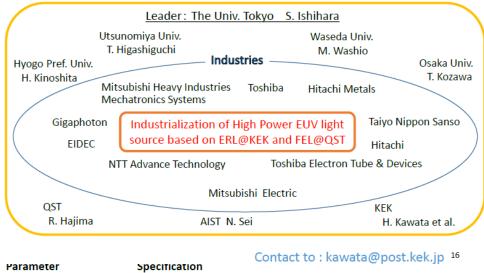
Input seed laser pulse

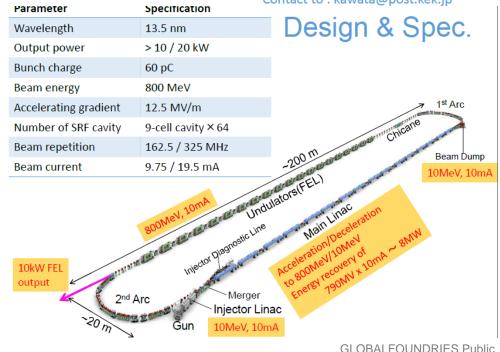
High energy prebunched e-bea

KEK

- Leverage Japan's industrial infrastructure
 - Fully integrated development
- cERL
- Expand upon known techniques
- Drive to the highest reward design
- Initial study group meeting...today!

EUV-FEL Light Source Study Group for Industrialization - since Aug. 2015 -



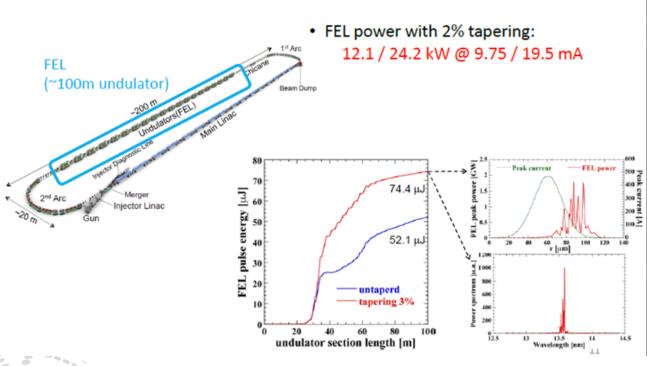


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cERL

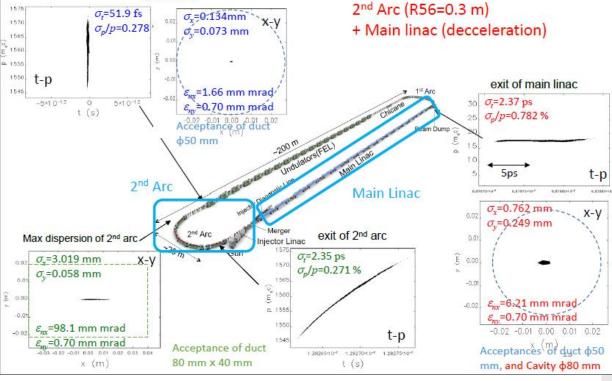
• Expand an existing toolset

FEL Performance



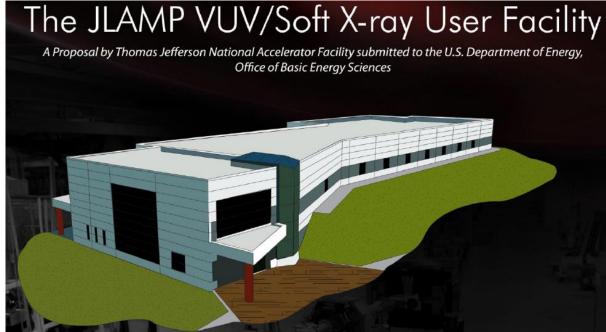
• 800 MeV, 60 pC, Freq.=162.5 / 325 MHz

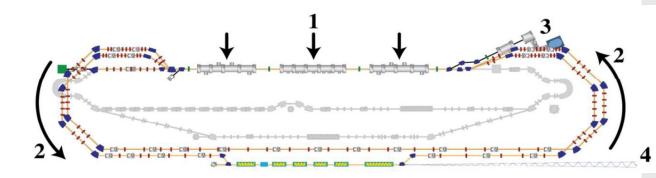
Bunch Decomp. and ER after FEL (elegant) exit of FEL



Jefferson Lab: JLAMP - An EUV FEL (2009 Proposal)

- Similar to c-ERL proposal
- Upgrades to existing facility:
 - Add advanced cryomodules (C100)
 - Add additional recirculation arc
 - Upgrade electron injector
 - Build EUV beamline and endstation
- ~\$120M to execute at existing JLab facility
- Adaptable to EUV lithography light source
 - Must scale to 10's kW output power
- After Proto?





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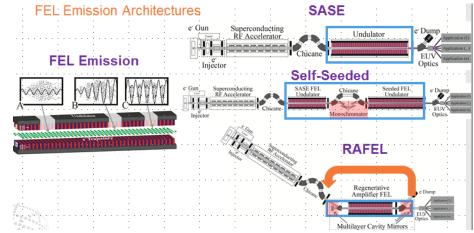


Evaluation of planned Lithography-based FEL Scorecard

- Baseline FEL emission architecture were defined and are currently being explored in detail
 - SASE
 - Evaluated for several parameters, more robust to fluctuations, higher variation in photon energy
 - **SS**
 - Improve monochromator design, evaluate similar parameters as with SASE
 - More sensitive to fluctuations
 - More critical parameters
 - RAFEL
 - Investigate feedback physics: explanation for not attaining steady-state power output



SPIE Advanced Lithography 2017



Metric	Bounds
Acc.; e-Beam Pointing Stability	± <mark>x</mark> μm
RF Power/Frequency Stability	± <mark>x</mark> W, ± <mark>x</mark> GHz
e ⁻ Beam Energy	± 0.25% dE/E
FEL; e-Beam Pointing Stability	± <mark>x</mark> μm
Magnetic Field	\pm 2E-4%, δK, δ ² K, φ
e ⁻ Bunch Emittance	ε < 0.3 mm mrad
EUV/e ⁻ Beam Matching	± e ⁻ BL/3
Output Pointing Stability	± <mark>x</mark> μm
Peak Intensity Maximum	x W/cm ²
Output Pulse Energy	±χμJ

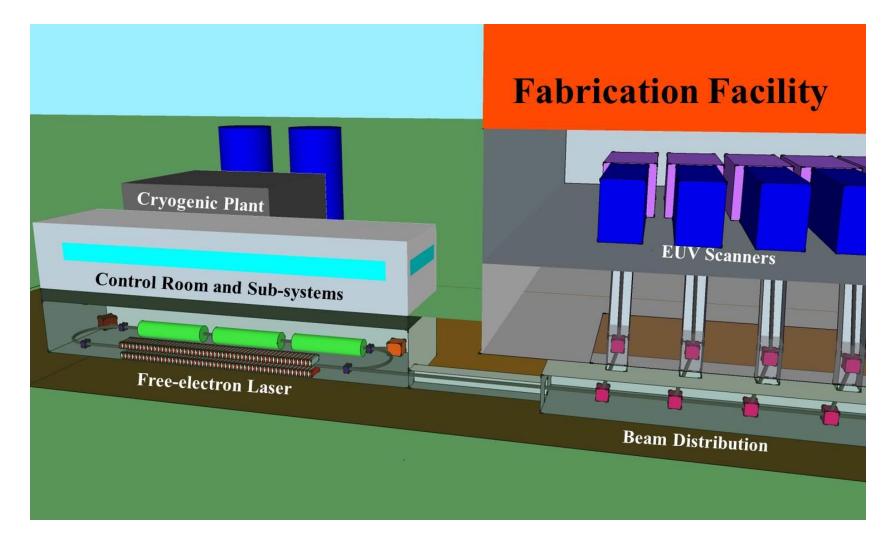
E.R. Hosler, O.R. Wood and M.E Preil. International EUVL Symposium. Maastrict, Netherlands. 2015.

Evolutionary Thinking: Research oriented to industrial FELs

- Single user \rightarrow Multi-end stations
- High peak power \rightarrow High average power
- Radiation permits \rightarrow Minimize radiation
- Many applications and extendibility \rightarrow Single purpose and power scaling
- Upgrades and scheduled maintenance blocks \rightarrow predictable, short service
- Constant facility development → First Time Right Design
- Value of the science vs. tax dollars \rightarrow Wafer cost

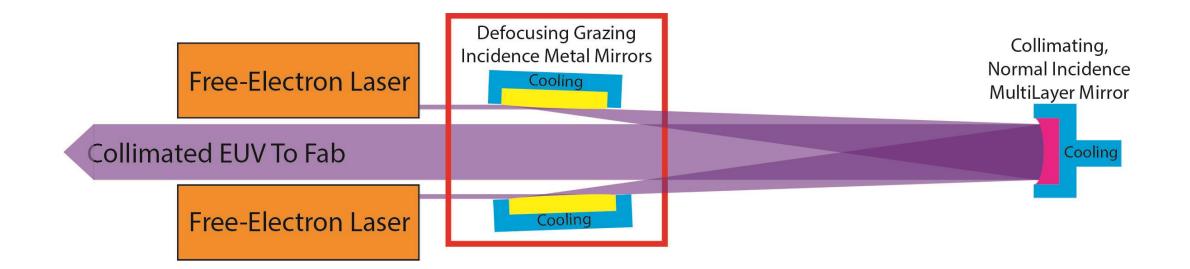
• How many more can we think of...?

Fab Integration



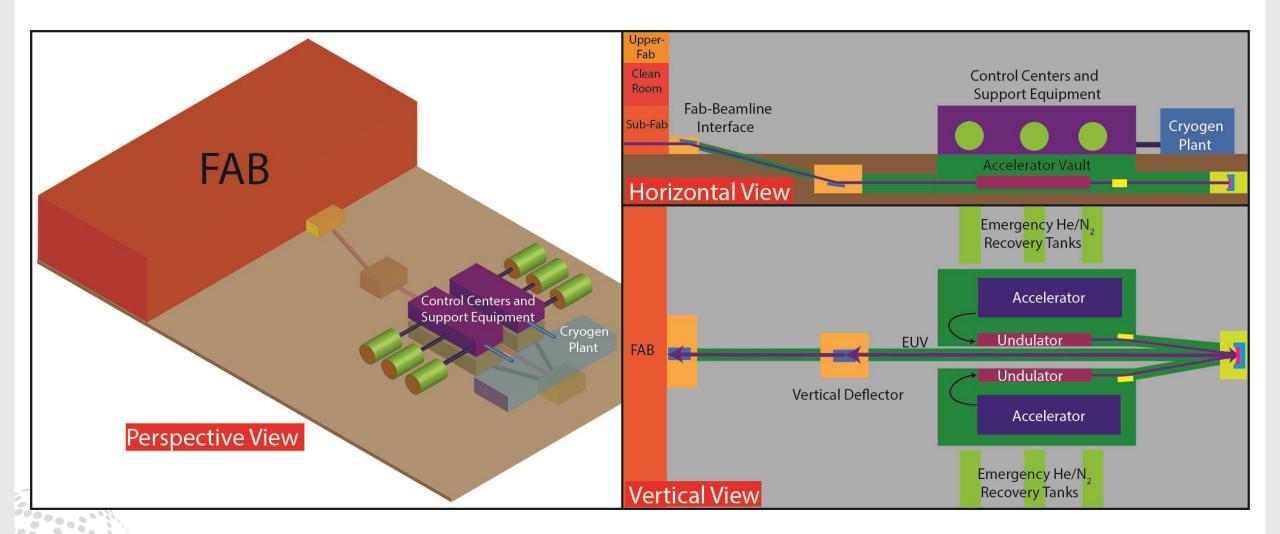
E.R. Hosler, O.R. Wood and M.E Preil. Extending extreme-UV lithography technology. SPIE Newsroom. 2016.

Redundant Operation



Method, apparatus and system for using free-electron laser compatible EUV beam for semiconductor wafer processing. US Patent Number 9,392,679 B2. Granted July 12, 2016.

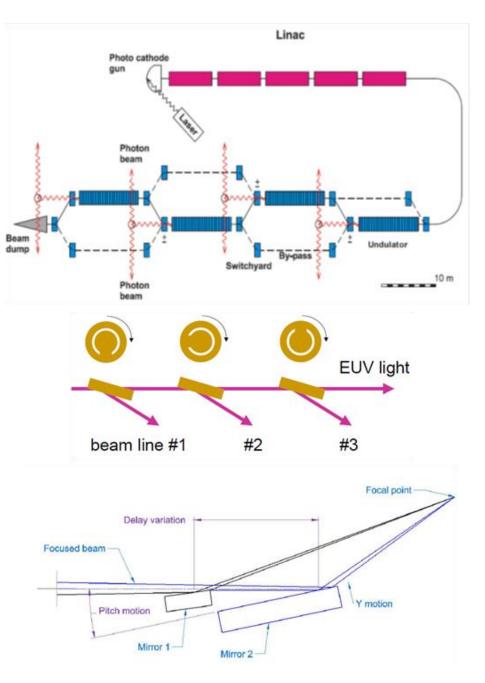
Redundant Operation



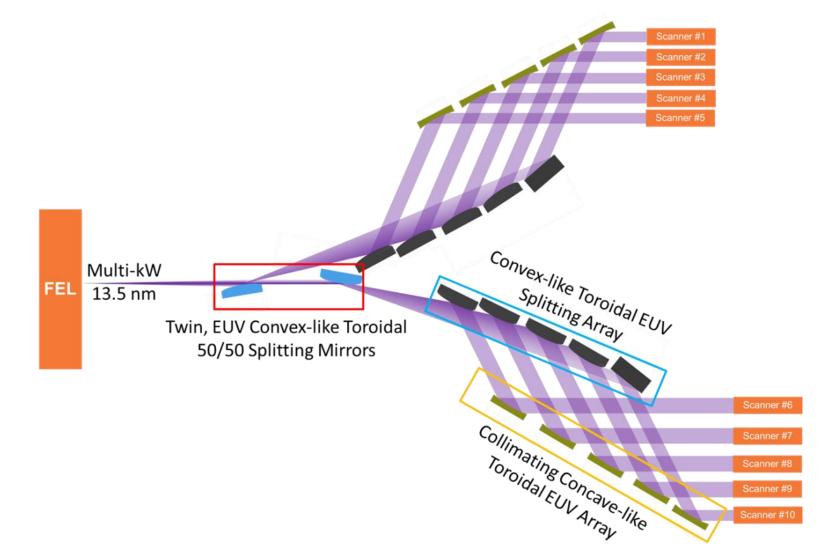
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Energy Distribution: Option 1 - Photons

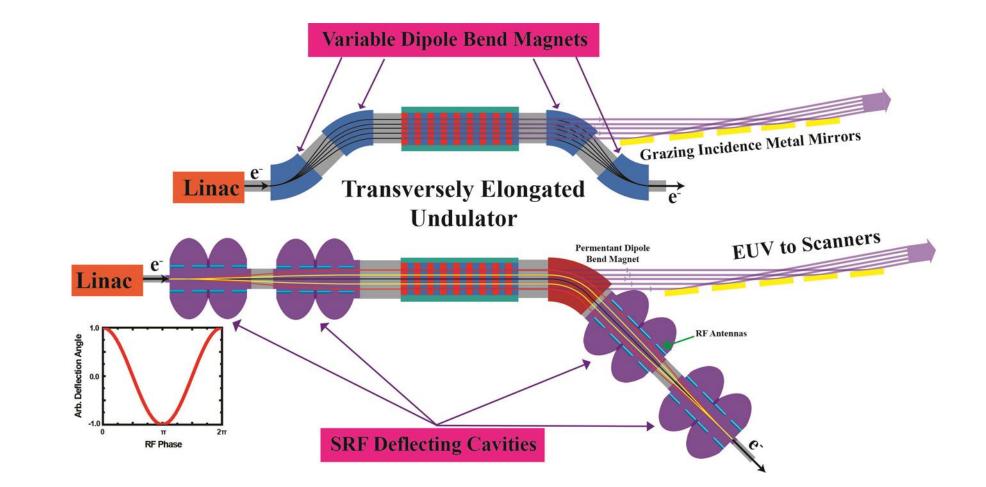
- 10x scanner to power...efficiency of beam distribution system may be the limiting factor
 - 10x scanners @ 1kW each
 - Beam distribution is 20% efficient...
 - 50kW required @ FEL output
- Multiple schemes proposed...
- Is there an optics-only solution?
- Is there a solution for the electron beam?



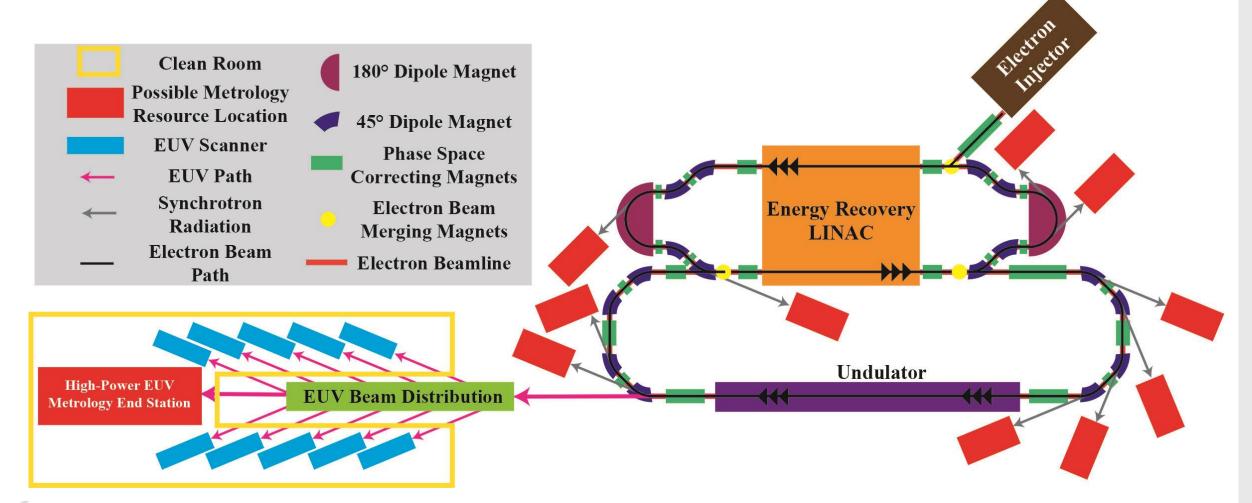
Energy Distribution: Option 1 - Photons



Energy Distribution: Option 2 - Electrons

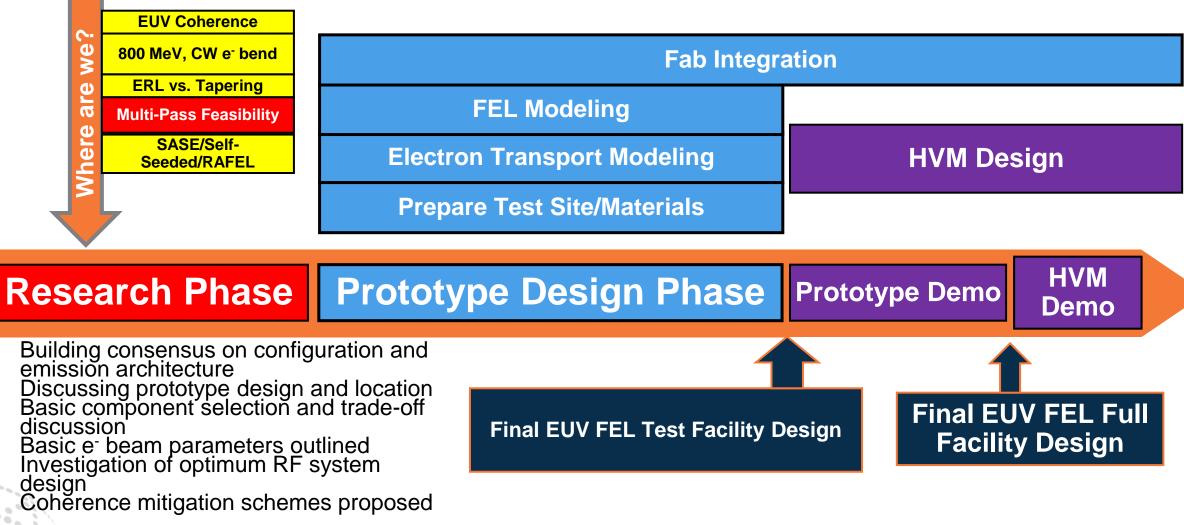


EUV Metrology



Summary

FEL Development Timeline Progress



Keens, S.G., Rossa, B., Frei, M., Free-electron lasers for 13nm EUV lithography: RF design strategies to minimise investment and operational costs. Proceedings of SPIE 9776, Extreme Ultraviolet (EUV) Lithography VI. 97760T. 2016.

Driving toward an industrial EUV FEL

- Success is two fold dependent
 - Development of new technology
 - Acceptance
- FEL/Accelerator Paradigm Shift
 - Power and availability are king
 - Single to many end stations
- Any development program must have a governance body
 - Semiconductor manufacturers
 - EUV Scanner Supplier(s)
 - Accelerator/FEL Research Team



Thank you!

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