Lattice Distortion near InGaP Compound Semiconductor Surface due to Surface Treatment of Bias Sputtering

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Introduction

III-V compound semiconductors are the basis of semiconductor lasers and optoelectronic devices. In device technology, bias sputtering on a semiconductor surface is often used as a surface cleaning technique. For a compound semiconductor, this technique might generate a compositional fluctuation near the surface and introduces a strain field to the surface.

The aim of this study is to know affections of bias sputtering as a surface cleaning technique to a strain field near an InGaP surface by the surface-sensitive X-ray diffraction.

Results and Discussion

The samples were prepared as mentioned below. Firstly, a 0.5- μ m Al_{0.7}Ga_{0.3}As layer and then a 0.5- μ m In_{0.48}Ga_{0.52}P layer were deposited on GaAs(001) substrate by using a metal organic chemical vapor deposition (MOCVD) method. Secondly, the resulting grown InGaP surface was cleaned by bias sputtering (Ar ion sputtering). Finally, an AlO_x layer was deposited on the surface at 573 K by a sputtering method using Al₂O₃ target materials. Four samples with different bias voltage for surface cleaning (0 V, 15 V, 43 V, and 77 V) were prepared.

An extremely asymmetric InGaP 113 reflection of the sample was measured to observe strain field. The X-rays impinged on the sample surface at a grazing angle (~0.2 °). The wavelength of the X-rays used in this measurement was about 0.145 nm.

Fig. 1 shows the measured rocking curves of the InGaP 113 reflection from the $AlO_x/InGaP$ interfaces at the four bias voltages used in the surface cleaning. The measured curves consist of a main peak as well as broad sub peaks to the right of this main peak. The intensity of the main peak depended on the bias voltage; except for a bias voltage of 43 V, the intensity decreased with increasing bias voltage. The highest peak intensity occurred for a bias voltage of 43 V.

Fig. 2 shows the calculated rocking curves using dynamical theory of X-ray diffraction for three different strains of topmost layer. In this calculation, the (001) atomic spacing was assumed to close to d_0 from $d_0(1+\varepsilon)$ as the depth reaches to 20 nm from the surface, if d_0 is the

(001) atomic spacing at bulk. Comparison between Fig. 1 and Fig. 2 reveals that, in general, bias sputtering introduced a tensile strain to a compound semiconductor surface. However, when the bias voltage was 43 V, a compressive strain was introduced. A possible reason for this compressive strain is removing of surface contaminations.

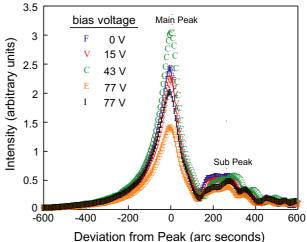


Fig. 1 Measured rocking curves of InGaP113 reflection.

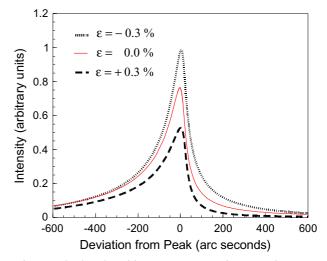


Fig. 2 Calculated rocking curves assuming a strain.

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